

PBA Design-for-Manufacturing Guideline

EDM-D-002 Electronic Component Specification for Printed Board Assembly

V1.4
August 2026

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The Design-for-eXcellence Guidelines principles

The PBA Design-for-eXcellence (DfX) Guidelines are designed to provide all electronic supply chain actors involved in the design, qualification, industrialization and production of Printed Board Assemblies practical guidelines to master the multi-disciplinary hardware aspects of electronic module realization and operation in a cost-effective way. The PBA DfX Guidelines are not electrical design guidelines. The PBA DfX guidelines provide the electrical designer the boundary conditions of industrial electronic manufacturing technology and operational reliability. It is intended to support the development of cost-effective, reliable PBA with a short time-to-market requiring a minimum number of design iterations.

Some of the characteristics of the PBA DfX Guidelines are:

- The PBA DfX Guidelines are oriented towards the overall optimization of the physical design of the final PBA based product.
- The guidelines refer to the relevant industry standards that are predominantly used in the international electronics industry such as those published by organizations as IPC and JEDEC. The guidelines do not replace industrial standards but define or recommend what options in the standards to use and will fill-in gaps if necessary. They provide the basis on which a company/product/product-line or application specific approach for design, industrialization and/or realization can be defined.
- Scientific argumentation and physical models form the basis of a large part of the guidelines and of the associated tools. This allows the use of the guidelines beyond the boundary of the users' experience domain. Therefore, it provides a powerful product and process innovation aid.
- The PBA DfX Guidelines will not specify, recommend or exclude specific brands of materials, components, suppliers or products. They will put forward minimal requirements on quality, physical and chemical properties and testing. They define and provide the DfManufacturing window for PBA realization.
- The PBA DfX Guidelines are based on verifiable physical models, standards and empirical data.

PBA DfX Guidelines Scope

- The PBA DfX guidelines cover lead-free SnAgCu and SnPb solder based assembly.
- The PBA DfX guidelines include: Design-for-Manufacturing, Design-for-Assembly, Design-for-Test, Design-for-Reliability, Design-for-RoHS, etc.

Acknowledgement

This document was realized in collaboration with the industrial and academic partners of imec's Center of Electronics Design & Manufacturing and Sirris.

Funding organizations

IWT is acknowledged for funding the VIS projects - especially the Collective Research project CO-PBA-DfX and the VIS-traject PROSPERITA - that have provided the scientific background for the PBA DfX Guidelines and gained the necessary industry support.

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1. Applicable Documents

This PBA DfX Guideline refers as part of the guideline to the most recent versions of the following documents and standards including their amendments.

EDM-D-003	PBA Assembly Material Specification
GR-78-CORE	Generic Requirements for the Physical Design and Manufacture of Telecommunications Products and Equipment
IPC-6012	Qualification and Performance Specification for Rigid Printed Boards
IPC-A-610	Acceptability of Electronic Assemblies
IPC-CC-830	Qualification and Performance of Electrical Insulating Compound for Printed Board Assemblies
IPC-CH-65	Guidelines for Cleaning of Printed Boards and Assemblies
IPC-CM-770	Guidelines for Printed Board Component Mounting
IPC-D-279	Design Guidelines for Reliable Surface Mount Technology Printed Board Assemblies
IPC-HDBK-001	Handbook and Guide to Supplement J-STD-001
IPC-HDBK-850	Guidelines for Design, Selection and Application of Potting Materials and Encapsulation Processes Used for Electronics Printed Circuit Board Assembly.
IPC-TM-650	IPC Test Methods
JEP-95	JEDEC registered and standard outlines for solid state and related products
JEP-155	Recommended ESD Target Levels for HBM/MM Qualification
JESD22-A121A	Test Method for Measuring Whisker Growth on Tin and Tin Alloy Surface Finishes
JESD201A	Environmental Acceptance Requirements for Tin Whisker Susceptibility of Tin and Tin Alloy Surface Finishes
JP-002	Current Tin Whiskers Theory and Mitigation Practices Guideline
JS-001	ESDA/JEDEC Joint Standard for Electrostatic Discharge Sensitivity Testing - Human Body Model (HBM) - Component Level
J-STD-001	Requirements for Soldering and Electronic Assemblies
J-STD-002	Solderability tests for Component leads, Terminations, lugs, terminals and Wires.
J-STD-020	Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices
J-STD-030	Guideline for Selection and Application of Underfill Material for Flip Chip and Other Micropackages
J-STD-033	Handling, Packing, Shipping and Use of Moisture/Reflow Sensitive Surface Mount Devices
J-STD-075	Classification of Non-IC Electronic Components for Assembly Processes
J-STD-609	Marking and Labeling of Components, PCBs and PCBAs to Identify Lead (Pb), Lead-Free (Pb-Free) and Other Attributes.
2011/65/EU	DIRECTIVE 2011/65/EU OF THE EUROPEAN PARLIAMENT AND OF THE COUNCIL of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (recast)
2011/37/EU	COMMISSION DIRECTIVE 2011/37/EU of 30 March 2011 amending Annex II to Directive 2000/53/EC of the European Parliament and of the Council on end-of-life vehicles
70/156/EEC	COUNCIL DIRECTIVE of 6 February 1970 on the approximation of the laws of the Member States relating to the type-approval of motor vehicles and their trailers